

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

 ATTY. DOCKET NO.
43876-144

 SERIAL NO.
10/616,303

 APPLICANT
Craig HANSEN, et al.

 FILING DATE
July 10, 2003

 GROUP
2463-2676

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MM		US 4,785,393	11/15/1988	Chu et al.	
		US 4,814,978	03/21/1989	Craig C. Hansen, et al.	
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						Yes	No

OTHER ART (including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MM		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)
MM		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)
MM		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
MM		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 08740-90038, (1990).
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MM	4,025,772	05/24/77	Constant			
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	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.
MM	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, July 31, 1995.

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MM	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.						
MM	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.						
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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translated	
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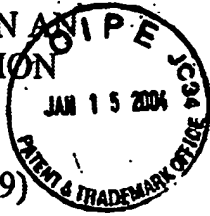
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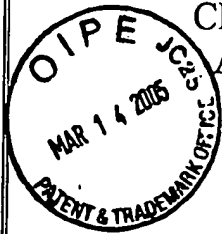
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
MM		EP 0 657 804 A1	08/14/95	Hewlett-Packard Company		
MM		EP 0 766 169 A1	4/2/1997	Matsushita Electric Industrial Co., Ltd.		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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		RUBY B. LEE, "Subword Parallelism with Max-2," IEEE Micro August 1996, pages 51-58.
		KOUHEI NADEHARA, ICHIRO KURODA, MASAYUKI DAITO, TAKASHI NAKAYAMA, "Low-Power Multimedia RISC," IEEE Micro December 1995, p.20-29

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